* **ADCACTSS**: Activate Sample Sequencer (1 bit for each sequencer – 4bits)
* **ADCRIS**: shows the status of the raw interrupt signal (1 bit for each sequencer – 4bits) - polling
* **ADCIM**: enable interrupts (1 bit for each sequencer – 4bits)
* **ADCISC**: write 1 to clear interrupt for (1 bit for each sequencer – 4bits)
* **ADCEMUX**: selects the event (trigger) that initiates sampling (4 bit for each sequencer – 16bits)
* **ADCSSMUX3**: choose input pin from table (4 bits)
* **ADCSSCTL3**: configure sample sequencer (4 individual bits)
* **ADCSSFSTAT3**: FIFO status (empty, full, hptr, tptr)
* **ADCSSFIFO3:** return the value of the FIFO
* **ADCPSSI**: initiate sampling in the sample sequencers (1 bit for each sequencer – 4bits)
* **ADCOSTAT**: indicates FIFO status (overflow or not) – must be cleared by writing 1 (1 bit for each FIFO - 4bits)
* **ADCTSSEL**: selects which PWM generator is the trigger
* **ADCSSPRI**: sets the priority for each sample sequencer (2bits each)
* **ADCSPC**: allows the ADC module to sample at one of 16 different discrete phases from 0.0°through 337.5°
* **ADCSAC**: controls the amount of hardware averaging applied to conversion results 2AVG (3bits)
* **ADCCTL**: configures the voltage reference (XXX)
* **ADCPP**: read ADC properties (speed, temp sensor, no of channels, resolution)
* **ADCPC**: sets ADC sample rate (4 speeds)
* **ADCCC**: controls the clock source for the ADC module.